



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Mark S. Moir et al.

Title: SOFTWARE TRANSACTIONAL MEMORY FOR DYNAMICALLY
SIZABLE SHARED DATA STRUCTURES

Application No.: 10/621,072

Filed: July 16, 2003

Examiner: Mano Padmanabhan

Group Art Unit: 2188

Atty. Docket No.: 004-8428

September 27, 2004

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- ☒ Form(s) PTO-1449 (2 pages), including copy(ies) of 33 reference(s).
☐ Other: n/a

to the Examiner's attention in the above-identified application. Citation of such information shall not be construed as:

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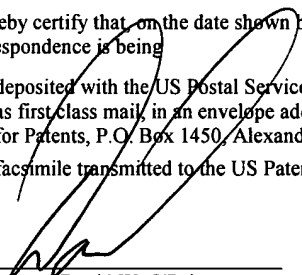
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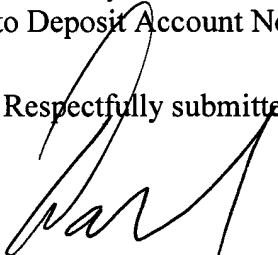
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- ☐ This Information Disclosure Statement is filed within three months of the filing date of a national application other than a continued prosecution application under § 1.53(d) or within three months of entry of the national stage as set forth in § 1.491 in an international application. Therefore, no fee is required.
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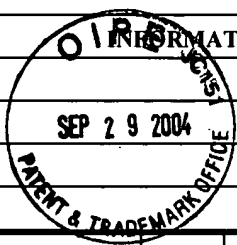
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David W. O'Brien	Date

Respectfully submitted,


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U.S. Department of Commerce, Patent and Trademark Office		Attorney Docket No.: 004-8428
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		Applicant(s): Moir et al.
(Use several sheets if necessary)		Filing Date: July 16, 2003
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NON PATENT LITERATURE DOCUMENTS		
*Examiner Initial	Cite No.	(Including name of author in capital letters, title of article, title of item, date, pertinent pages, volume-issue number(s), publisher, city and/or country where published.)
	1	Afek, Yehuda et al., "Long-Lived Renaming Made Adaptive", 18 th Annual ACM Symposium on Principles of Distributed Computing, pages 91-104, 1999.
	2	Afek, Yehuda, "Wait-Free Made Fast", 27 th Annual ACM Symposium on Theory of Computing, pages 538-547, 1995.
	3	Agesen, Ole et al., "DCAS-Based Concurrent Deques", 12 th Annual ACM Symposium on Parallel Algorithms and Architectures, pages 137-146, July 2000.
	4	Anderson, James H. et al., "Using Local-Spin k-Exclusion Algorithms to Improve Wait-Free Object Implementations", 12 th Annual ACM Symposium on Principles of Distributed Computing, November 1995 (revised 1996, 1997).
	5	Arora, Nimar S. et al., "Thread Scheduling for Multiprogrammed Multiprocessors", 10 th Annual ACM Symposium on Parallel Algorithms and Architectures, pages 119-129, 1998.
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	12	Greenwald, Michael B., "Non-Blocking Synchronization and System Design", PhD Thesis, Stanford University Technical Report STAN-CS-TR-1624, Palo Alto, California, August 1999.
	13	Herlihy, Maurice, "A Methodology for Implementing Highly Concurrent Data Objects", ACM Transactions on Programming Languages and System, pages 745-770, November 1993.
	14	Herlihy, Maurice, "Dynamic-Sized Lockfree Data Structures", Sun Microsystems Technical Report SMLI TR-2002-112, June 2002.
	15	Herlihy, Maurice et al., "Linearizability: A Correctness Condition for Concurrent Objects", ACM Transactions on Programming Languages and Systems, pages 463-492, July 1990.
	16	Herlihy, Maurice et al., "The Repeat Offender Problem: A Mechanism for Supporting Dynamic-Sized Lock-Free Data Structures", Sun Microsystems Technical Report SMLI TR-2002-112, June 2002.
Examiner		Date Considered
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.</p>		

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Applicant(s): Moir et al.
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	17	Herlihy, Maurice et al., "Transactional Memory: Architectural Support for Lock-Free Data Structures", 20 th International Symposium in Computer Architecture, 1993.
	18	Herlihy, Maurice et al., "Obstruction-Free Synchronization: Double-Ended Queues as an Example", 23 rd International Conference on Distributed Computing, May 2003.
	19	Israeli, Amos et al., "Disjoint-Access-Parallel Implementations of Strong Shared Memory Primitives", 13 th Annual ACM Symposium on Principles of Distributed Computing, pages 151-160, 1994
	20	Lamport, Leslie, "How to Make a Multiprocessor Computer that Correctly Executes Multiprocess Programs", IEEE Transactions on Computers, September 1979.
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